

CALL FOR PAPERS

Journal of Microelectronic Manufacturing (JoMM)
Special Issue on **CAD Technologies**

Guest Editor Shiuh-Wuu Lee



Dr. Shiuh-Wuu Lee worked in the semiconductor industry for 37 years with 20 years at Intel Corporation as Intel Fellow and director, 8 years at AT&T Bell Laboratories as member of technical staff, 6 years at SMIC as executive vice president and general manager of Technology Development, 1.5 years at Synopsys as special consultant and 1 year at Zhejiang Laboratory as director of its chip design center. Dr. Shiuh-Wuu Lee received Ph.D. degree in Electrical & Computer Engineering from the University of Michigan in 1980, published 61 technical journal and conference papers, and delivered numerous keynote and invited speeches at major industry forums and international technical conferences.

Topic: Computer simulation technologies critical to the development and optimization IC technologies for manufacturing and product design.

“With continued shrinking in technology feature sizes, fast increasing complexity in process and lithography and the pressing need for new materials to continue the drive for performance, low power and density, multitudes of critical CAD technologies in modeling and simulation technologies have become indispensable. This special issue is dedicated to highlighting the background, current status, applications and future direction in some of the key enabling CAD technologies”.

Scope:

- TCAD (Technology Computer Aided Design)
- Computation Lithography
- DTCO (Design Technology Co-optimization)
- Material Modeling
- CAD for Manufacturing & Testing

Submission Information:

- Papers targeting the special issue should be submitted through the JoMM submission system (<http://jommpublish.org/>) or sent by email (jomm@jommpublish.org), and will undergo a similar review process as regularly submitted papers.
- Deadline for submission is September 30th, 2020.
- Manuscript length and format info is available at the website.

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